

SOT-23



Pin Definition: 1. Gate

2. Source

3. Drain

PRODUCT SUMMARY

V _{DS} (V)	R _{DS(on)} (mΩ)	I _D (A)
	28 @ V _{GS} = 10V	5.8
30	33 @ V _{GS} = 4.5V	5.0
	52 @ V _{GS} = 2.5V	4.0

Features

- Advance Trench Process Technology
- High Density Cell Design for Ultra Low On-resistance

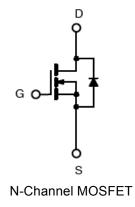
Application

- Load Switch
- PA Switch

Ordering Information

Part No.	Package	Packing	
TSM3400CX RF	SOT-23	3Kpcs / 7" Reel	

Block Diagram



Absolute Maximum Rating (Ta = 25°C unless otherwise noted)

Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	V _{DS}	30	V	
Gate-Source Voltage	V _{GS}	±12	V	
Continuous Drain Current	I _D	5.8	А	
Pulsed Drain Current	I _{DM}	30	А	
Continuous Source Current (Diode Conduction) ^{a,b}	I _S	2.5	А	
Maximum Power Dissipation @ Ta = 25° C	P _D	1.4	W	
Operating Junction Temperature	TJ	+150	°C	
Operating Junction and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C	

Thermal Performance

Parameter	Symbol	Limit	Unit
Junction to Foot Thermal Resistance	R Θ_{JF}	70	°C/W
Junction to Ambient Thermal Resistance (PCB mounted)	RƏ _{JA}	90	°C/W

Notes:

a. Pulse width limited by the Maximum junction temperature

b. Surface Mounted on FR4 Board, t \leq 10 sec.



Electrical Specifications (Ta = 25°C unless otherwise noted)

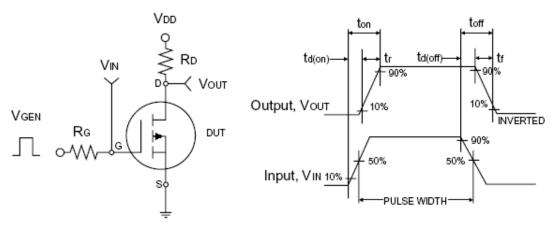
Parameter	Conditions	Symbol	Min	Тур	Мах	Unit
Static				·	I	L
Drain-Source Breakdown Voltage	$V_{GS} = 0V, I_{D} = 250 \mu A$	BV _{DSS}	30			V
Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	V _{GS(TH)}	0.7		1.4	V
Gate Body Leakage	V_{GS} = ±12V, V_{DS} = 0V	I _{GSS}			±100	nA
Zero Gate Voltage Drain Current	$V_{DS} = 24V, V_{GS} = 0V$	I _{DSS}			1.0	μA
On-State Drain Current	V_{DS} = 5V, V_{GS} = 4.5V	I _{D(ON)}	20			А
	V _{GS} = 10V, I _D = 5.8A		23	28		
Drain-Source On-State Resistance	V _{GS} = 4.5V, I _D = 5A	R _{DS(ON)}		28	33	mΩ
	V_{GS} = 2.5V, I_{D} = 4A			43	52	
Forward Transconductance	$V_{DS} = 5V, I_{D} = 5A$	g _{fs}	10	15		S
Diode Forward Voltage	I _S = 1.0A, V _{GS} = 0V	V _{SD}		0.76	1.0	V
Dynamic ^b						
Total Gate Charge		Qg	-	9.7	12	
Gate-Source Charge	$V_{DS} = 15V, I_D = 5.8A,$ $V_{GS} = 10V$	Q _{gs}	-	1.63		nC
Gate-Drain Charge	V _{GS} – 10V	Q_gd	-	3.1		
Input Capacitance		C _{iss}		857	1030	
Output Capacitance	V _{DS} = 15V, V _{GS} = 0V, f = 1.0MHz	C _{oss}		97		pF
Reverse Transfer Capacitance		C _{rss}		71		
Switching ^c						
Turn-On Delay Time		t _{d(on)}		3.3	5	
Turn-On Rise Time	$V_{DD} = 15V, R_L = 1.8\Omega,$	t _r		4.7	7	20
Turn-Off Delay Time	$I_{\rm D} = 1$ A, $V_{\rm GEN} = 10$ V,	t _{d(off)}		26	39	nS
Turn-Off Fall Time	$R_{G} = 6\Omega$	t _f		4.1	6.2	

Notes:

a. pulse test: PW \leq 300µS, duty cycle \leq 2%

b. For DESIGN AID ONLY, not subject to production testing.

b. Switching time is essentially independent of operating temperature.



Switching Test Circuit

Switchin Waveforms

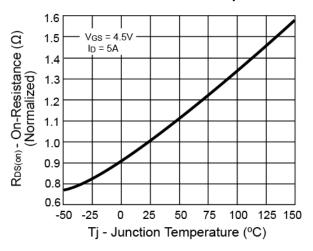


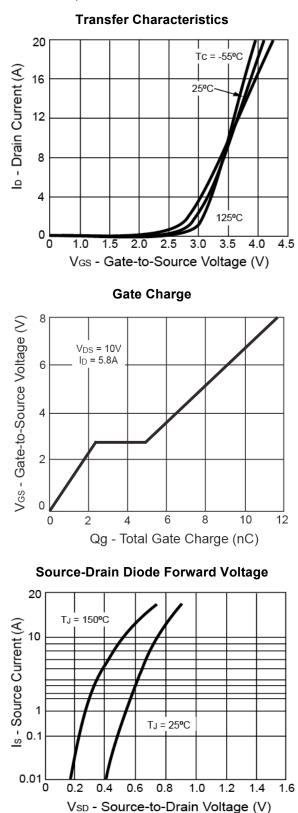
Output Characteristics 30 25 Ip - Drain Current (A) V_{GS} = 4.5V thru 10V 4V 20 15 3.5V 10 3V 0 0 2 3 4 5 1 VDs - Drain-to-Source Voltage (V) **On-Resistance vs. Drain Current** 100 $R^{DS(on)}$ - On-Resistance (Ω) 80 60 Vgs = 2.5V 40 V_{GS} = 4.5V 20 V_{GS} = 10V 0 12 0 4 8 16 20

Electrical Characteristics Curve (Ta = 25°C, unless otherwise noted)

On-Resistance vs. Junction Temperature

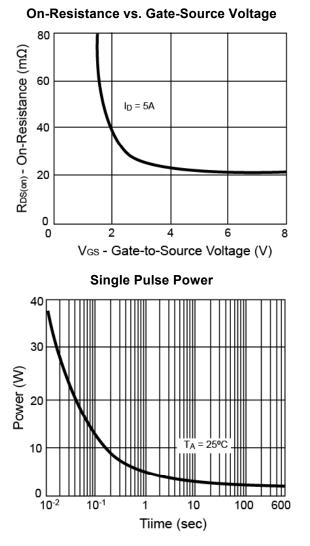
ID - Drain Current (A)

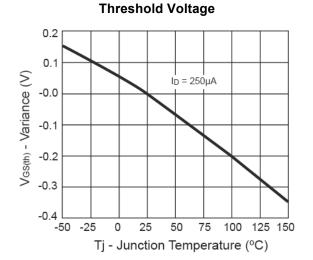




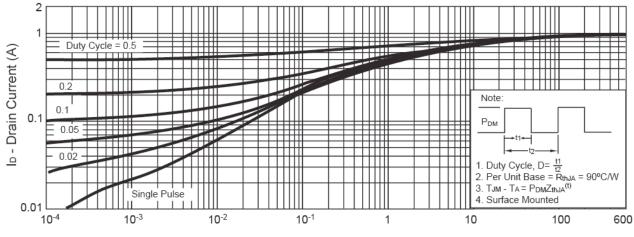


Electrical Characteristics Curve (Ta = 25°C, unless otherwise noted)





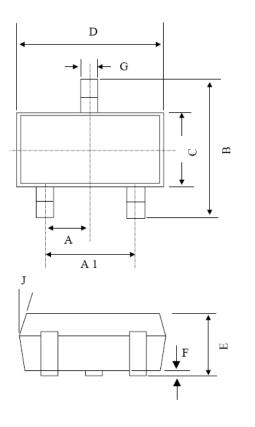
Normalized Thermal Transient Impedance, Junction-to-Ambient

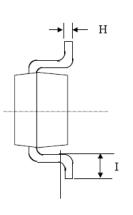


Square Wave Pulse Duration (sec)



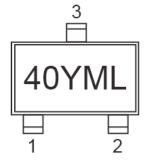
SOT-23 Mechanical Drawing





	SOT-23 DIMENSION					
DIM	MILLIMETERS		INCHES			
DIN	MIN	MAX	MIN	MAX.		
А	0.95	0.95 BSC		BSC		
A1	1.9	BSC	0.074	BSC		
В	2.60	3.00	0.102	0.118		
С	1.40	1.70	0.055	0.067		
D	2.80	3.10	0.110	0.122		
E	1.00	1.30	0.039	0.051		
F	0.00	0.10	0.000	0.004		
G	0.35	0.50	0.014	0.020		
Н	0.10	0.20	0.004	0.008		
I	0.30	0.60	0.012	0.024		
J	5°	10°	5°	10°		

Marking Diagram



- 40 = Device Code
- Y = Year Code
- **M** = Month Code
 - (A=Jan, B=Feb, C=Mar, D=Apl, E=May, F=Jun, G=Jul, H=Aug, I=Sep, J=Oct, K=Nov, L=Dec)
- L = Lot Code



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